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SFW

In re Patent Application of: Donald Malcolm MacIntyre

Atty Docket No. MCSP-101

Application No.: 09/045,507

Filed: March 20, 1998

Confirmation No.: 1010

For: WAFER SCALE SEMICONDUCTOR STRUCTURE

M/S ISSUE FEE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Transmittal herewith is an amendment in the above-identified application.

The fee has been calculated as shown below.

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	10	MINUS	51	0	x \$50 =	\$0.00
INDEP.	2	MINUS	3	0	x \$200 =	\$0.00
FIRST PRESENTATION OF MULTIPLE DEP CLAIMS					+ \$360	\$0.00
					TOTAL	\$0.00

Small Entity 50% Filing Fee Reduction (if applicable)

\$0.00

- \* If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3  
\*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.  
\*\*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space. The "Highest Number Previously Paid For" (Total or Independent is the highest number found from the equivalent box in Col. 1 of a prior amendment or the number of claims originally filed.)

- ☒ Attached for filing: Response to PTO Notice of Drawing Inconsistency with Specification Mailed July 5, 2006
- ☐ A check in the amount of \$\_\_\_\_\_ is attached.
- ☒ Please charge any additional fees, including any fees necessary for extensions of time or credit overpayment to Deposit Account No. 50-1703, under Order No. MCSP-101.  
**A duplicate copy of this sheet is enclosed.**
- ☒ Attached for filing is an Proposed Amendment Under CFR 1.312.

STALLMAN & POLLOCK LLP

Dated: July 11, 2006

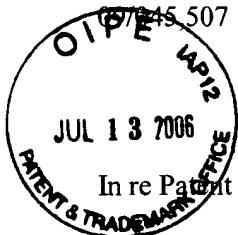
By: Michael J. Pollock  
Michael J. Pollock (Reg. No. 29,098)  
Attorneys for Applicant(s)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 11, 2006.

Dated: 07/11/2006

By: Tanisha Lawrence-Caceres  
Tanisha Lawrence-Caceres



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Donald Malcolm Macintyre

Application No.: 09/045,502

Filed: March 20, 1998

For: WAFER SCALE SEMICONDUCTOR  
STRUCTURE

Confirmation No.: 1010

Group Art Unit: 2814

Examiner: Wai Sing Louie

**RESPONSE TO PTO NOTICE OF DRAWING  
INCONSISTENCY WITH SPECIFICATION  
MAILED JULY 5, 2006**353 Sacramento Street, Suite 2200  
San Francisco, CA 94111  
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STALLMAN &amp; POLLOCK LLP

Dated: 07/11/2006

By:

Tanisha Lawrence-Caceres

Sir:

In response to the Notice of Drawing Inconsistency with Specification mailed July 5, 2006, please  
amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.**Remarks** begin on page 3 of this paper.